

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**In re Application of :**

Alexander Tetelbaum

**Serial No. :**

09/827,434

**Group Art Unit :**

2825

**Filed :**

April 06, 2001

**Examiner :**

Bowers, Brandon W.

**For :**

Wire Delay Distributed Model

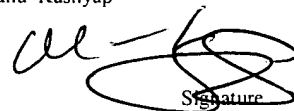
**Atty Docket :**

/ 00-294

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

2/9/05  
Date

  
Signature

**SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85**

**Official Draftsman**

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

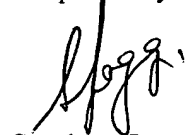
Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
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408-433-7475

Date: 2/9/05

Respectfully submitted,



Sandeep Jaggi

Reg. No. 43,331